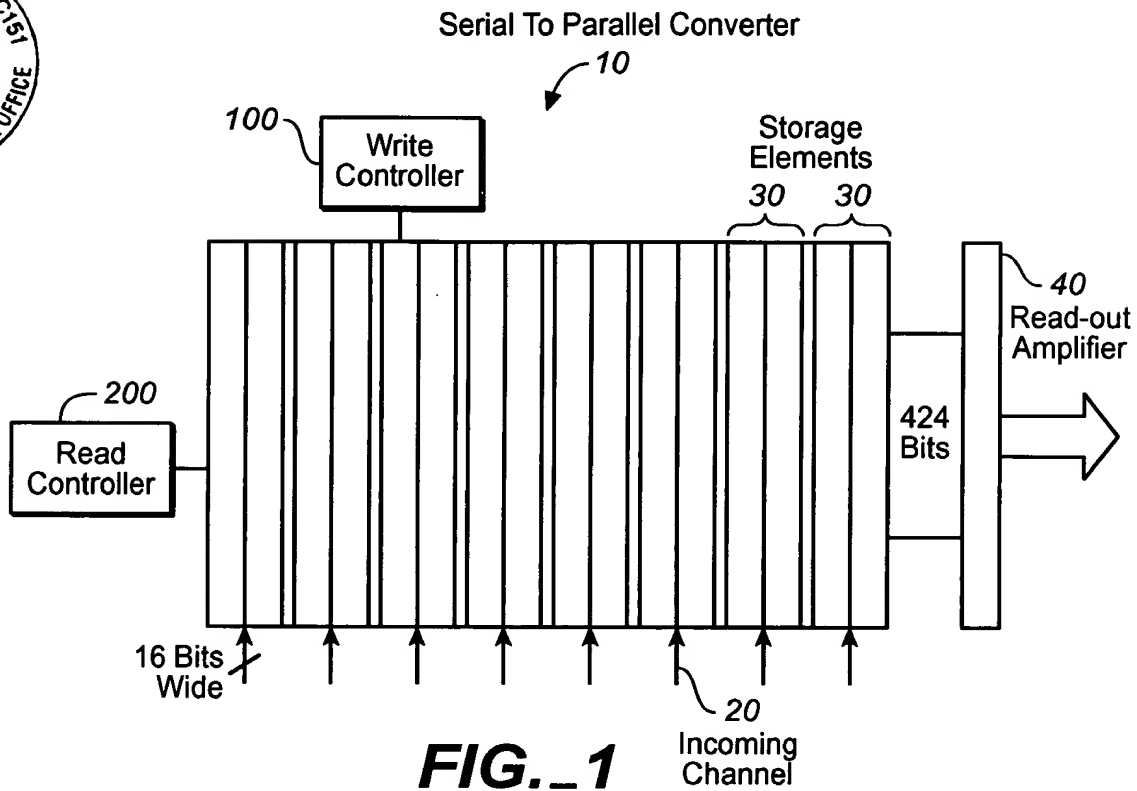


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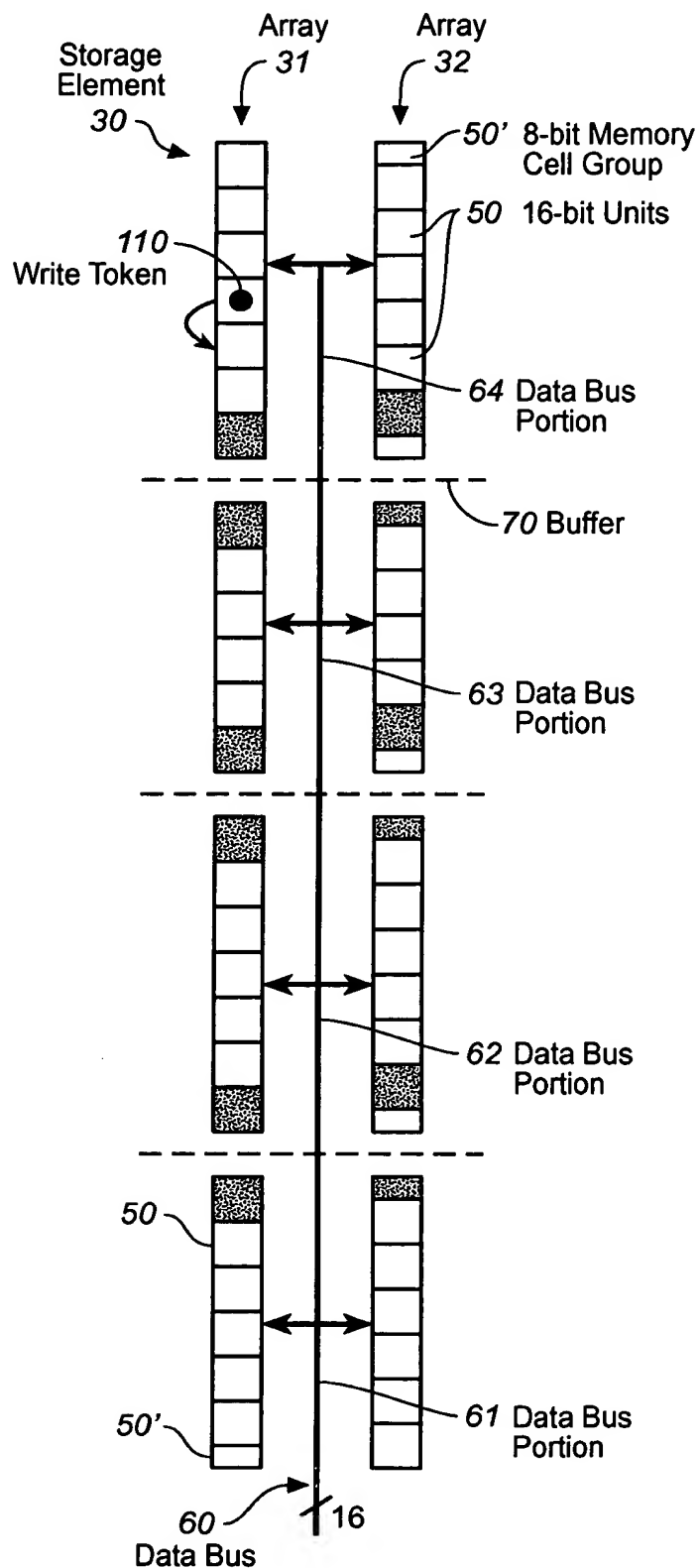


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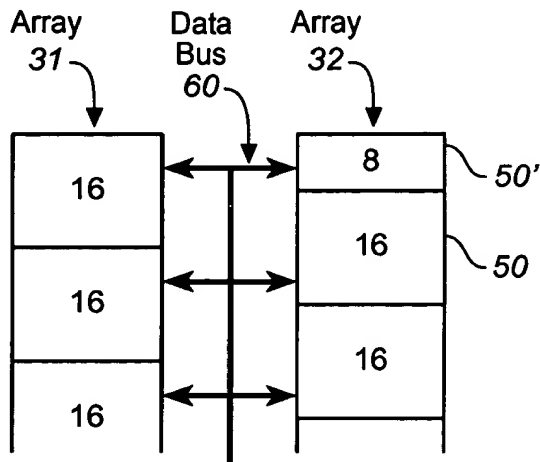
**FIG. 2**

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SERIAL FORMAT TO PARALLEL FORMAT AND VICE VERSA

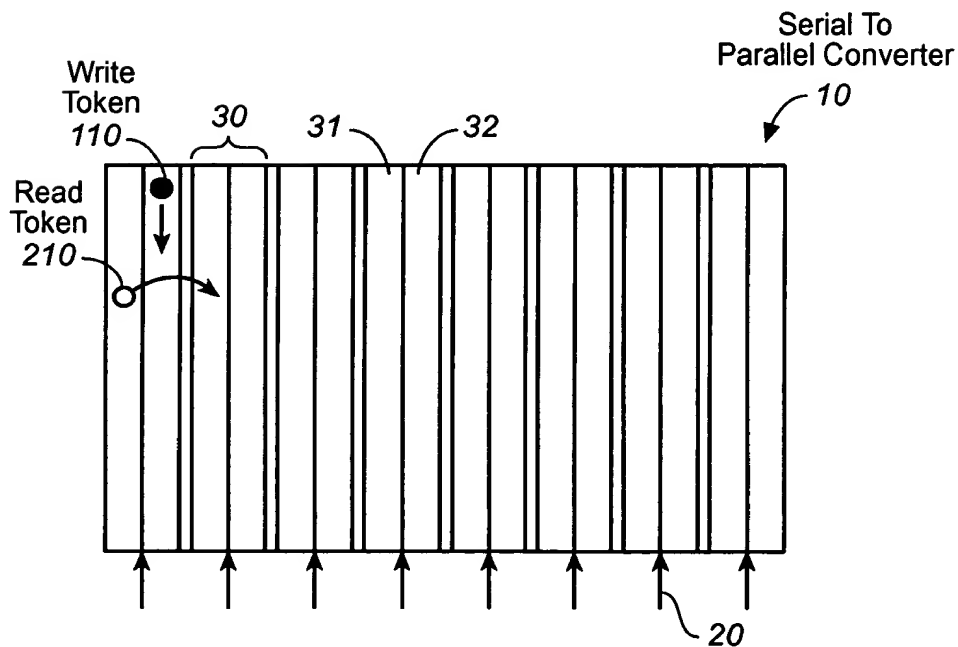
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**FIG.\_3**



**FIG.\_4**

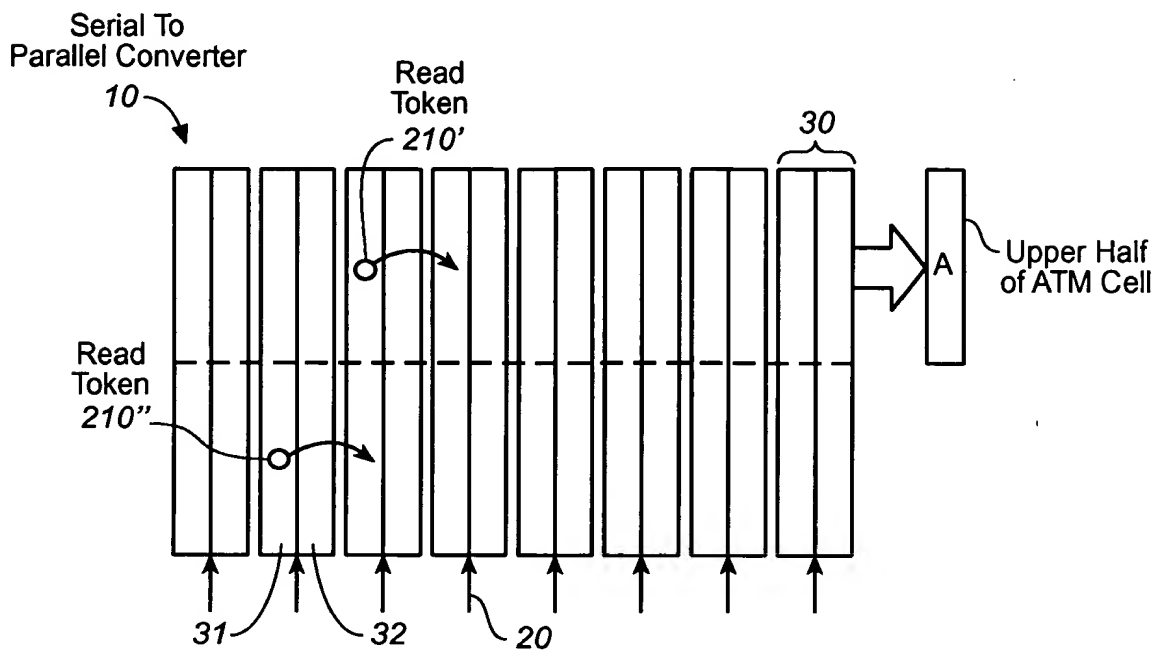


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SERIAL FORMAT TO PARALLEL FORMAT AND VICE VERSA

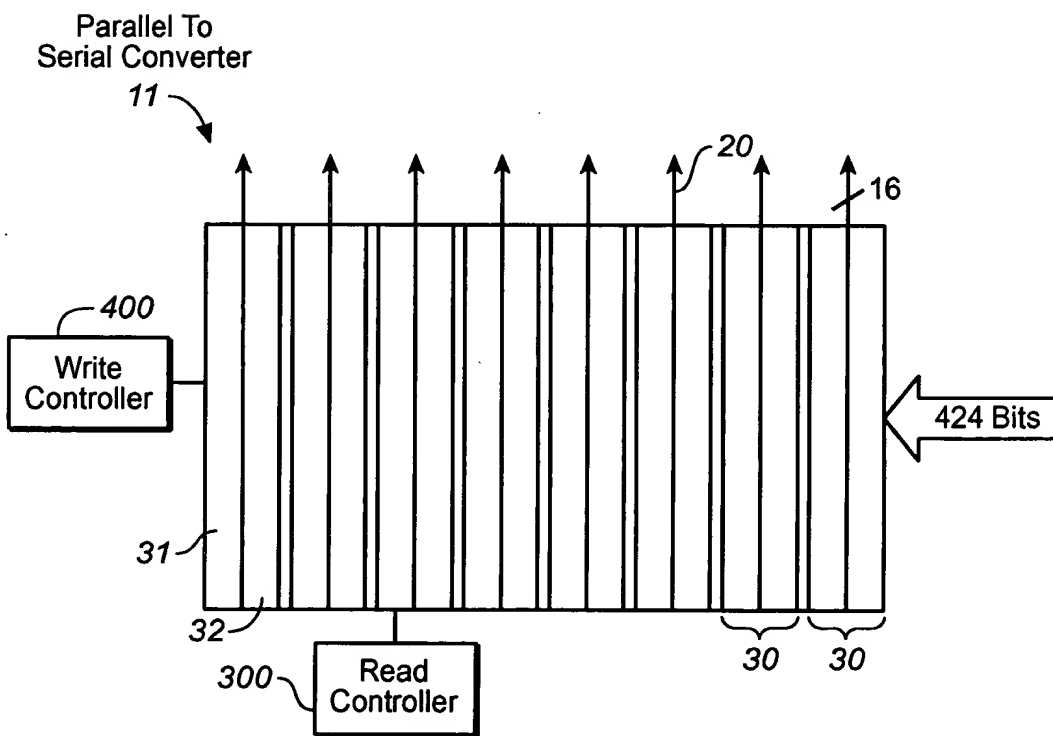
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**FIG.\_5**



**FIG.\_6**